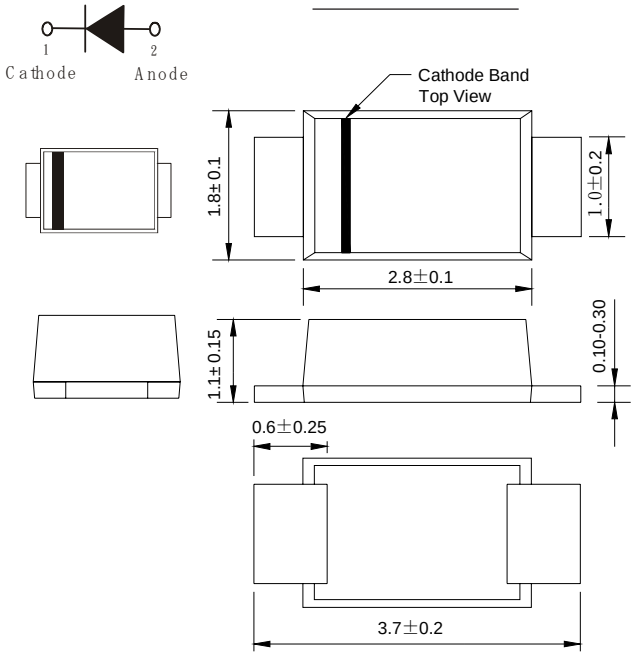
CURRENT 1.0 Ampere
VOLTAGE RANG 50 to 1000 Volts

A1 THRU A7

FEATURES

Glass passivated device
Ideal for surface mouted applications
Low reverse leakage
Metallurgically bonded construction
High temperature soldering guaranteed:
250℃/10 seconds,0.375”(9.5mm) lead length,
5 lbs. (2.3kg) tension

SOD-123FL



Dimensions in millimeters

MECHANICAL DATA

Case: JEDEC SOD-123FL molded plastic body over passivated chip
Terminals: Solderable per MIL-STD-750, Method 2026
Polarity: Color band denotes cathode end
Mounting Position: Any
Weight:0.0007 ounce, 0.02 grams

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25 ambient temperature unless otherwise specified.
Single phase, half wave ,60Hz, resistive or inductive load.
For capacitive load, derate current by 20%

TYPE NUMBER		D1	D2	D3	D4	D5	D6	D7	UNITS
		A1	A2	A3	A4	A5	A6	A7	
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current @T_A=75	$I_{(AV)}$	1.0							A
Peak Forward Surge Current 8.3ms Single Half Sine-Wave Super Imposed on Rated Load(JEDEC Method)	I_{FSM}	30							A
Maximum Forward Voltage at 1.0A DC	V_F	1.1							V
Maximum DC Reverse Current @T_J=25 at Rated DC Blocking Voltage @T_J=100	I_R	5 50							μA
Typical Junction Capacitance (Note1)	C_J	10							pF
Typical Thermal Resistance (Note2)	R_{JA}	30							/W
Operating Temperature Range	T_J	-55 to +125							
Storage Temperature Range	T_{STG}	-55 to +125							

NOTES:1.Measured at 1.0 MHz and applied reverse voltage of 4.0V DC.
2.Thermal resistance junction of ambient.

CURRENT 1.0 Ampere
VOLTAGE RANG 50 to 1000 Volts

A1 THRU A7

RATING AND CHRACTERISTIC CURVES A1 Thru A7

FIG.1- MAXIMUN FORWARD CURRENT DERATING CURVE

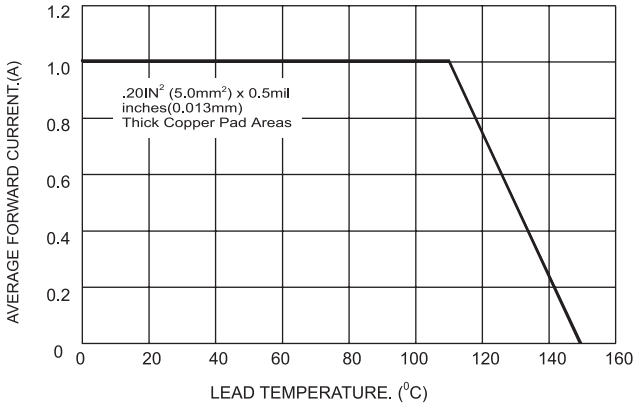


FIG.2-MAXIMUN NON-REPETITIVE FORWARD SURGE CURRENT

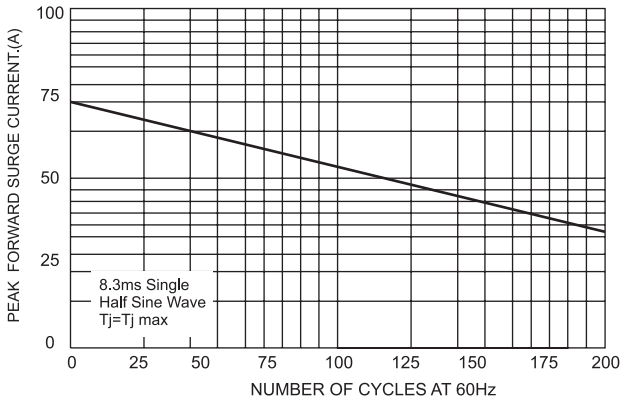


FIG.4-TYPICAL REVERSE CHARACTERISTICS

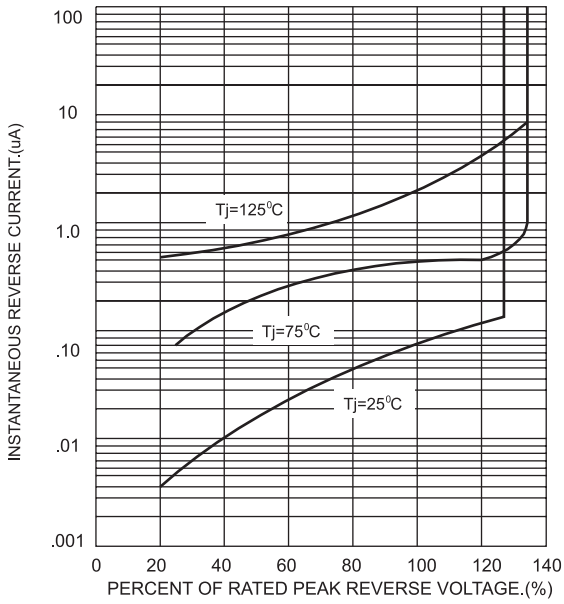


FIG.3-TYPICAL FORWARD CHARACTERISTICS

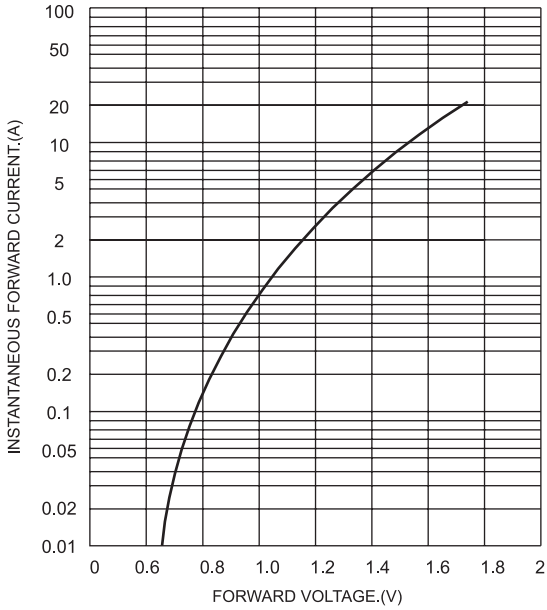


FIG.5-TYPICAL JUNCTION CAPACITANCE

